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RESPONSE UNDER 37 C.F.R. § 1.116  
EXPEDITED PROCEDURE - EXAMINING GROUP 2800

PATENT

I hereby certify that on the date specified below, this correspondence is being deposited with the United States Postal Service as first-class mail in an envelope addressed to Mail Stop AF, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

July 13, 2004  
Date

AJ Shaikh  
Ayesha J. Shaikh

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Appl. No. : 10/057,205	Confirmation No. : 1725
Applicants : Sion C. Quinlan and Tim J. Bales	
Filed : January 25, 2002	Attorney Docket No.: 500986.02
Art Unit : 2829	Customer No. : 27,076
Examiner : Scott B. Geyer	
Title : SEMICONDUCTOR PACKAGE ASSEMBLY AND METHOD FOR ELECTRICALLY ISOLATING MODULES	

Mail Stop AF  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**RESPONSE UNDER 37 C.F.R. § 1.116**

Sir:

Applicants acknowledge receipt of the Office Action dated April 16, 2004.

Please amend the above-identified application as follows:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks begin on page 4 of this paper.